

25 de noviembre de 2025

1st Workshop of Spanish Chip Chairs

11:00	E. Villar and Institutional Representative (TBC)	Session 1: Opening
11:30	Chair: J.C. López	Session 2: Multi-Domain Approaches
11:30	M. Lopez <i>The UPM-Indra Chair on Microelectronics: Bridging Devices, Circuits, and Architectures to build the Future of Integrated Systems</i>	
11:35	A. Rubio <i>Chip Chair UPC: Chips for Advanced Architectures and Photonic Systems</i>	
11:40	F. J. Ballester <i>UPV-VaSiC Chair in Microelectronic Design</i>	
11:45	E. M. Segura <i>Progress from the Cátedra Malaga Microelectronics</i>	
11:50	A. J. Acosta <i>USECHIP: Chair of Microelectronics at the University of Seville</i>	
11:55	Wrap-up: Pros & Cons of Multi-Domain Chairs	
12:25	Coffee Break	
12:55	Chair: F.J. Ballester	Session 3: System Design
12:55	J. C. Jimeno <i>KatuTxipi and its hidden relation with Chips and Chairs</i>	
13:00	A. Astarloa <i>SOC4SENSING: boosting Microelectronics in the Industrial Eco-System of the Basque Country</i>	
13:05	P. López <i>USC-Televés Chair in Microelectronics</i>	
13:10	E. Villar <i>Cantabria Chip Chair: Heterogeneous System Design</i>	
13:15	J. C. López <i>UCLM PERTE Chip Chair</i>	
13:20	Wrap-up: Addressing System Design	
13:50	Networking Lunch	

14:50	Chair: P. López	Session 4: Microelectronic & Photonic Technologies
14:50	C. Reig <i>Universitat de València: Advanced Materials for the Semiconductor and Integrated Circuit Industry</i>	
14:55	F. Gámiz <i>Atomically thin, exceptionally powerful: 2D materials in More-than-Moore Electronics</i>	
15:00	C. de Dios & M. Portela <i>Cátedras ePIQ UC3M – ARQUIMEA Research Center, to foster the application of integrated photonics and electronics to the new quantum technologies</i>	
15:05	P. Muñoz <i>Cátedra PIC-UPV</i>	
15:10	R. Berenguer <i>SISPI: Microelectronic Design of Wireless, Battery-less Sensors</i>	
15:15	<i>Wrap-up: Technology Training & Research</i>	
15:45	C.G. Triviño <i>Joint Report of Activities of the PERTE Chip Chairs</i>	Session 5: Chip Chairs Cooperation in Microelectronics Research and Training
16:05	Coffee Break	
16:35	Chair: E. Villar	
16:35	W. Nobel <i>Chipdesign Germany: Industrial - Academic network to increase sovereignty</i>	Session 6: International Liaisons
16:50	M. Demicheli <i>CHIPS.IT: Bridging Research, Training, and Industry in Microelectronics</i>	
17:05	H. F. Tellez <i>IMEC-Spain, Europractice and the European Chip Design Platform: Fostering Microelectronics Innovation in Europe</i>	
17:20	MTDyFP - TBC MicroNanoSpain - A. Gabarrón INDRA - E. Casanueva Spanish Chip Chairs - A. Rubio ChipsDesign Germany - W. Nebel CHIPT-IT - M. Demicheli IMEC - H. F. Tellez	Session 7: Panel - International Cooperation in Microelectronics Research and Training
18:35	E. Villar and next Organizer	Closing
19:05	Networking Dinner	
21:00		