



1st Workshop of Spanish Chip Chairs

25 November 2025, Santander, Spain

CALL FOR PARTICIPATION

The conference on Design of Circuits and Integrated Systems (DCIS) is an international meeting for researchers in the highly active fields of micro- and nano-electronic circuits and integrated systems. It provides an excellent forum to present and discuss works on the emerging challenges offered by technology, in the areas of modeling, design, implementation and test of devices, circuits and systems.

DCIS 2025 topics of interest include:

- Analog / mixed signal circuits and systems
- Design of power-, thermal- and variability-aware circuits and systems
- New computing and hardware paradigms (machine learning, AI acceleration, approximate and stochastic computing, bio-inspired computing, ...)
- Circuits and systems in advanced and emerging technologies (SiC, GaN, silicon photonics, 3DICs, quantum devices, ...)
- Reconfigurable devices and systems (FPGA, NVRAM, memristors, ...)
- Embedded and high-performance computing.
- RISC-V-based systems (hardware, architectures, software, ...)
- EDA tools and methods
- Industrial and power electronics
- Sustainable computing and systems
- Sensory circuits and systems
- Energy management and harvesting
- Test, fault tolerance, reliability and modeling
- Radiofrequency ICs (6G, RFID, NFC, ...)
- On-Chip and off-chip interconnects
- IoT and applications (industry 4.0, personalized healthcare, ...)
- Hardware security
- Educational Methods on Electronics

We encourage submission of manuscripts (IEEE single-spaced, double-column, A4 page, 6 pages maximum) employing the IEEE manuscript templates for conference proceedings through the online paper submission system at the conference website.

“Best papers on Embedded HW” will be invited to submit an extended version of the conference paper to the Journal on Microprocessors and Microsystems: Embedded Hardware Design (MICPRO). ”

Important Dates

Deadline for submission: ~~May 4th, 2025~~ **June 8th, 2025**

Notification of acceptance: July 20th, 2025

Deadline for final paper submission: September 7th, 2025

Registration open: July 20th, 2025

Paper submission: <https://easychair.org/conferences/?conf=dcis2025>



General Chair

Eugenio Villar (University of Cantabria)

General Co-Chairs

Marcelino Santos (SiliconGate)

Program Chair and Co-Chair

Frederic Petrot (TIMA) , Pablo Sánchez (U. Cantabria)

Special Sessions Chair

Francesc Moll (BSC)

Publicity Chair

Jesús Barba (UCLM)

Awards Chair

Francisco Fernández (University of Sevilla)

Ph.D. Forum Chair

Celia López (University Carlos III)

Industrial Liasons Chair

Alfonso Gabarrón (AESEMI)

Local Committee Chair and Committee

Víctor Fernández (Chair), Yolanda Lechuga, Héctor Posadas, José Ángel Miguel, David Aledo, Raúl Gómez-Varela, Andrés Martínez, Pablo Montes, Marina Cordevilla, José Luis Vazquez

International Committee IEEE Liasons

Shanti Pavan, Orla Feely, Andreas Demosthenous, Jinhu Lu, Pau-Choo (Julia) Chung, Sorin Cotofana, Sergio Bampi, Alyssa Apsel, Chai Wah Wu, José Pineda de Gyvez, Gabriel Rincón-Mora, Massimo Alioto, Ana Rusu, David Atienza, Wenwu Zhu.

Steering Committee

Eduard Alarcon, María José Avedillo de Juan, Roc Berenguer, Luis Entrena, Francisco Javier González Cañete, Jaime Jiménez, Celia López Ongil, Juan Carlos López, Marisa López-Vallejo, Josep Maria López Villega, José Machado da Silva, Francesc Moll, Antonio Nuñez, Arantxa Otín, Luis Parrilla, Michel Renovell, Teresa Riesgo, Miquel Roca, Angel Rodríguez-Vazquez, Marcelino Santos, Roberto Sarmiento, José A. Silva Matos, J.Paulo Teixeira, Antonio J. Torralba, Javier Uceda, Eugenio Villar.

Venue

DCIS 2025 will be held at Hotel Santemar,
Joaquín Costa, 28. 39005 Santander, Spain
Hotel.santemar@hsantos.es +34 942272900

Sponsors:   

Web page: dcis2025.unican.es

Organized by:



Supported by:



Contact: c3_secretaria@unican.es